

FIGURE 1 (PRIOR ART) Inventor: Varadarajan Devnath Atty. Docket No. P05762

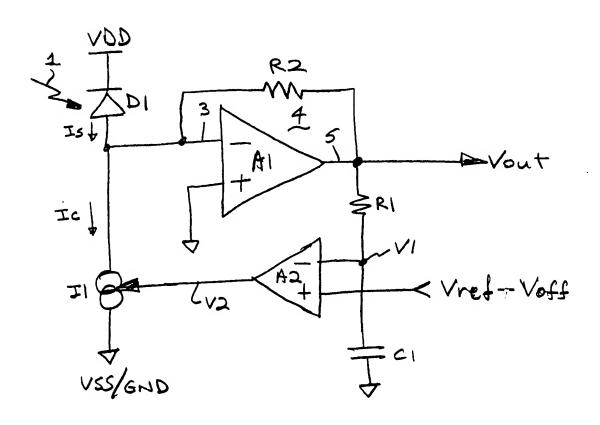
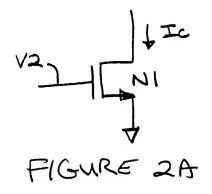


FIGURE 2



TRANSIMPEDANCE AMPLIFIER WITH CONTROLLABLE NOISE REDUCTION

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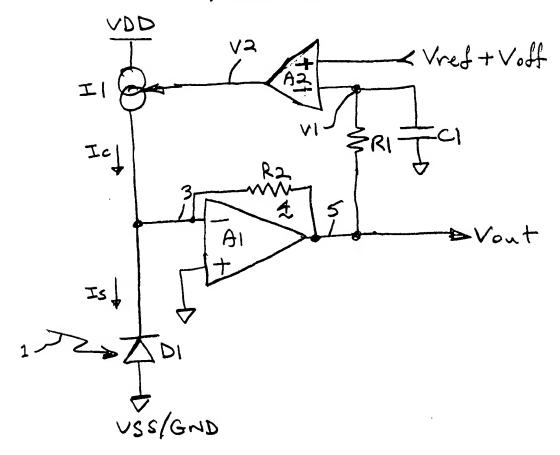
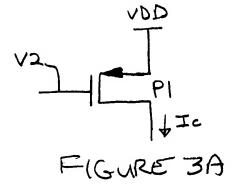


FIGURE 3



TRANSIMPEDANCE AMPLIFIER WITH CONTROLLABLE NOISE REDUCTION

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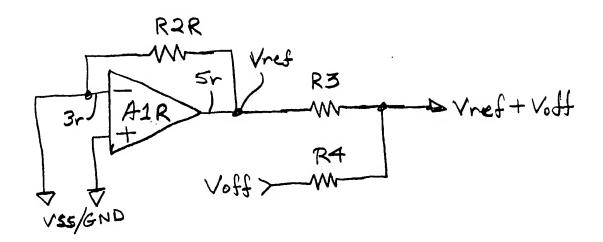


FIGURE 4